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|  | HAL Software Lifesciences Test | Date: 25/10/2015 | |
| | | Doc no.: | |
| | | | |

Spike


PH_Setup

Testing documentation

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|  | HAL Software Lifesciences Test | Date: 25/10/2015 | |
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| | | | |

APPROVALS

| Functional Area | Required Approver | Date |
|---------------------|-------------------|------|
| Author / Originator | Print Name: | |
| | Sign Name: | |
| Engineering | Print Name: | |
| | Sign Name: | |
| Operations | Print Name: | |
| | Sign Name: | |
| Automation | Print Name: | |
| | Sign Name: | |
| QAV | Print Name: | |
| | Sign Name: | |

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Revision History

| Revision | Date | Author | Description |
|----------|------|--------|-------------|
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Reference Documentation

| Document | Ref. | Title |
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1. PH_Setup

| Cycle number | Expected results | Actual results | Result (Pass/Fail) | Initials | Date | Comments |
|--------------|---------------------------|----------------|--------------------|----------|------|----------|
| | Confirm SFC is as per FDS | | | | | |

1.1.1. Complete

1.1.2. Stopped

1.1.3. Stopping

1.1.4. Running

| Steps | Cycle number | Expected results | Actual results | Result (Pass/Fail) | Initials | Date | Comments |
|-------|--------------|---|----------------|--------------------|----------|------|----------|
| S0 | | Logic executes as per specification in FDS. | | | | | |
| S1 | | Logic executes as per specification in FDS. | | | | | |
| S2 | | Logic executes as | | | | | |



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| Steps | Cycle number | Expected results | Actual results | Result (Pass/Fail) | Initials | Date | Comments |
|-------|--------------|---|----------------|--------------------|----------|------|----------|
| | | per specification in FDS. | | | | | |
| S3 | | Logic executes as per specification in FDS. | | | | | |
| S4 | | Logic executes as per specification in FDS. | | | | | |
| S5 | | Logic executes as per specification in FDS. | | | | | |
| S6 | | Logic executes as per specification in FDS. | | | | | |
| S7 | | Logic executes as per specification | | | | | |



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| Steps | Cycle number | Expected results | Actual results | Result (Pass/Fail) | Initials | Date | Comments |
|-------|--------------|---|----------------|--------------------|----------|------|----------|
| | | in FDS. | | | | | |
| S8 | | Logic executes as per specification in FDS. | | | | | |
| T0 | | Logic executes as per specification in FDS. | | | | | |
| T1 | | Logic executes as per specification in FDS. | | | | | |
| T10 | | Logic executes as per specification in FDS. | | | | | |
| T11 | | Logic executes as per specification in FDS. | | | | | |




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| Steps | Cycle number | Expected results | Actual results | Result (Pass/Fail) | Initials | Date | Comments |
|-------|--------------|---|----------------|--------------------|----------|------|----------|
| T2 | | Logic executes as per specification in FDS. | | | | | |
| T3 | | Logic executes as per specification in FDS. | | | | | |
| T4 | | Logic executes as per specification in FDS. | | | | | |
| T5 | | Logic executes as per specification in FDS. | | | | | |
| T6 | | Logic executes as per specification in FDS. | | | | | |
| T7 | | Logic | | | | | |

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| | | | | | | | |
| Steps | Cycle number | Expected results | Actual results | Result (Pass/Fail) | Initials | Date | Comments |
| | | executes as per specification in FDS. | | | | | |
| T8 | | Logic executes as per specification in FDS. | | | | | |
| T9 | | Logic executes as per specification in FDS. | | | | | |

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|---------------------|--|-------------|--|
| Recorded By | | Date | |
| Witnessed By | | Date | |
| Reviewed By | | Date | |

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1.1.5. Idle

1.1.6. Restarting

1.1.7. Held

1.1.8. Holding


1.1.9. Aborting

1.1.10. Aborted

2. *Parameter Verification*

| Parameter type | FDS reference | Expected result | Result as Expected (Yes/No) | Fault no. Verified By/Date |
|-----------------------|----------------------|------------------------|--|---|
| | | | | |
| | | | | |

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|---------------------|--|-------------|--|
| Recorded By | | Date | |
| Witnessed By | | Date | |

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| Reviewed By | | Date | |